On-chip biasing of the common-gate stages in the cascodes is provided by connecting their gates to the forward voltage of a Schottky diode. The current density through these stages is designed so that the $V_{ds}$ is sufficiently negative to maintain the previous devices, $M_2$ and $M_3$, in saturation. On-chip decoupling capacitors are used at the gates of the cascode devices.

Fig. 3 Die photograph of GaAs IC amplifier mounted in microwave package

Experimental results: The amplifier (Fig. 3) was fabricated as part of a multiproject chip reticle at TriQuint Semiconductor using a depletion-mode GaAs MESFET process. Table 1 summarises the characteristics of this technology. The measured insertion gain of the amplifier (Fig. 4) indicates a DC gain of 26 dB and a $-3$ dB bandwidth of 3.2 GHz in good agreement with the simulated response. The input and output VSWR (Fig. 4) are less than 2.5:1 over the passband. The measured noise figure (Fig. 5) is between 5.5 dB and 6.5 dB over the midband range of frequencies. The amplifier exhibits a saturated output power of 10 dBm and an output power of 8 dBm at the $-1$ dB gain compression point. Nominal power dissipation for the circuit is 850 mW. The measured performance of the amplifier is summarised in Table 2.

Table 1 TYPICAL MESFET PARAMETERS FOR $W = 300 \mu m$, $L = 1 \mu m$

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{DS}$</td>
<td>42 mA</td>
</tr>
<tr>
<td>$V_{DS}$</td>
<td>$-1.5$ V</td>
</tr>
<tr>
<td>$g_m$</td>
<td>40 mS</td>
</tr>
<tr>
<td>$C_{gd}$</td>
<td>475 fF</td>
</tr>
<tr>
<td>$C_{gs}$</td>
<td>40 fF</td>
</tr>
<tr>
<td>$C_{ds}$</td>
<td>75 fF</td>
</tr>
<tr>
<td>$R_P$</td>
<td>450 $\Omega$</td>
</tr>
<tr>
<td>$f_T$</td>
<td>13 GHz</td>
</tr>
</tbody>
</table>

Table 2 AMPLIFIER PERFORMANCE

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Gain</td>
<td>26 dB</td>
</tr>
<tr>
<td>Bandwidth</td>
<td>3.2 GHz</td>
</tr>
<tr>
<td>Worst input VSWR</td>
<td>2.5:1</td>
</tr>
<tr>
<td>Worst output VSWR</td>
<td>2.5:1</td>
</tr>
<tr>
<td>Noise figure</td>
<td>6.5 dB</td>
</tr>
<tr>
<td>$P_{1-dB}$</td>
<td>8 dBm</td>
</tr>
<tr>
<td>Saturation power</td>
<td>10 dBm</td>
</tr>
<tr>
<td>Power dissipation</td>
<td>850 mW</td>
</tr>
<tr>
<td>Die size</td>
<td>$1 mm \times 1 mm$</td>
</tr>
</tbody>
</table>

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References

NONLINEAR VIBRATIONS AND HYSTERESIS OF MICROMACHINED SILICON RESONATORS DESIGNED AS FREQUENCY-OUT SENSORS

Indexing terms: Resonators, Optical sensors

Experimental observation of nonlinear vibrations and hysteresis of micromachined silicon resonators is reported. The experimental results are explained using a simple model in which the restoring force acting in the resonator contains a small cubic term. The effects will impose a limit to the maximum amplitude which can be excited while still maintaining reliability of these devices as frequency-output sensors.

Introduction: Sensor applications of micromachined silicon mechanical resonators are of great interest because of their frequency output, and to avoid any electromagnetic interference it is desirable to have both optical activation and
optical interrogation. Interferometric displacement detection systems provide the necessary sensitivity to deal with an amplitude of vibration as small as a few nanometres, but these systems may require special compensation or isolation techniques to avoid any perturbation induced by the environment. These difficulties can be reduced significantly by using a non-interferometric technique for interrogation, and this has the added advantage that multimode fibre and low-coherence sources can be used. However, such systems are less sensitive and will require the excitation of relatively large amplitudes of the silicon mechanical resonator, i.e. of the order of micrometres.

We report in this letter the observation of a nonlinear response, and hysteresis, of micromachined silicon resonators when large amplitudes of vibration are excited. This can clearly limit the application of the non-interferometric system for interrogation.

Measurements have been made on a number of micromachined silicon resonators of different geometries, a simple beam, a double-ended tuning fork and a coupled pair of suspended rectangular plates. The results shown here refer to this last device since its higher Q-factor makes it easier to obtain accurate measurements of its characteristics, but the same qualitative behaviour has been observed in the others.

**Experimental arrangement:** Fig. 1 shows a diagram of the resonator and its holder. The device which includes this resonator is designed as a pressure transducer and is fully described elsewhere.

![Diagram of resonator](image)

**Experimental results:** Several resonances have been detected and analysed in the frequency range up to 1 MHz, and each exhibits the expected Lorentzian response of amplitude against frequency when small amplitudes of the order of nanometres are excited. The Q-factor of each resonance can be determined from the width of these curves. When the amplitude of vibration is increased, by increasing the voltage applied to the piezoelectric transducer, the response curve becomes slightly asymmetrical, and eventually develops hysteresis if the amplitude is increased sufficiently.

Fig. 2 shows an example which corresponds to the 97.9 kHz resonance. When the amplitude of oscillation is small, the peak of this resonance is at 97.310 kHz, but when the amplitude is increased the peak shifts towards higher frequencies. The experimental points correspond to an excited peak amplitude of 2.3 μm. If the applied frequency is increased from below resonance, the amplitude increases smoothly up to 2.3 μm and then decreases discontinuously to some negligible value at the frequency of 97.683 kHz. For decreasing frequency the amplitude-frequency relation follows the lower line, the amplitude increasing as the frequency approaches 97.310 kHz, and jumps discontinuously to the top curve at the frequency of 97.334 kHz. Further decrease in the frequency follows the top curve back down the original path.

Two different types of hysteresis, characterised by the shift of the resonance frequency, have been observed. Most of the resonances of the coupled pair of rectangular plates, as well as the resonances of the beam, shift towards higher frequencies as the amplitude of vibration is increased, but the observed resonances of the double-ended tuning fork shift towards lower frequencies.

![Resonance curve with hysteresis](image)

**Theory and discussion:** A single resonance of a mechanical structure can be described by the equation

\[ \xi^2 = \frac{\xi_0^2}{1 + \left( \frac{\omega - \omega_0}{h} \right)^2} \]  

(1)

where \( \xi_0 \) is the amplitude of vibration as a function of the frequency \( \omega \), \( \omega_0 \) is the peak value of \( \xi_0 \) at the resonant frequency \( \omega_0 \), and \( h \) is a small damping coefficient.

The hysteresis behaviour described earlier can be explained in terms of a slightly anharmonic response of the vibrators. If the restoring force \( R \) of the system is a slightly nonlinear function of the displacement \( \xi \), i.e. let us assume a small cubic contribution, then the frequency of resonance \( \omega_0 \) will exhibit a small quadratic dependence on the amplitude of vibration \( \xi_0 \).

Both deviations from the harmonic vibrator can be expressed in terms of a small coefficient \( \beta \), as follows:

\[ R = -\omega^2_0 \xi_0 + \beta \xi^3 \]  

(2a)

\[ \omega_0 = \omega_{00}(1 + \frac{\beta}{3} \xi_0^2) \]  

(2b)

where \( \omega_{00} \) is the frequency of resonance when small amplitudes are excited and hence the nonlinearity of the system is negligible. The deviation from linear behaviour is small, eqn. 1 still applies, but with \( \omega_0 \) being given by eqn. 2b.

Eqn. 2b shows that the shift of the experimental resonance curves towards higher or lower frequencies depends on whether \( \beta \) is positive or negative. A positive coefficient \( \beta \) corresponds to what is referred to as a hard nonlinearity and a negative \( \beta \) corresponds to a soft nonlinearity.

The system develops hysteresis if the shift of the frequency of resonance exceeds a certain limit fixed by the bandwidth. Eqn. 1, with \( \omega_0 \) given by eqn. 2b, becomes three-valued within a range of frequencies, and exhibits two frequencies at which \( \xi_0^2/\omega_0^2 \) becomes infinite, i.e. where hysteresis takes place. The relation between these frequencies and \( \xi_0 \) determines the minimum value of the peak amplitude required to develop hysteresis. This amplitude is given by

\[ \xi^2 > \frac{8h}{3\omega_0^2 |\beta|} \]  

(3)

To apply this model to a particular resonance, the two parameters \( h \) and \( \beta \) have to be determined. The damping coefficient can be calculated by measuring the quality factor \( Q \) of the resonance when small amplitudes are excited, \( Q = \omega_0^2/2h \). The coefficient \( \beta \) can be determined by fitting eqn. 2b to a set of experimental measurements of the peak amplitude \( \xi_0^2 \) and the corresponding values of the frequency of resonance \( \omega_0 \), obtained by driving the piezoelectric transducer at different voltages.
The 97kHz resonance shown in Fig. 2 exhibits a quality factor \( Q = 21000 \) and a coefficient \( \beta = 1.94 \times 10^{-3} \mu m^{-2} \). Taking into account these values, the theoretical curve derived from eqns. 1 and 2b, and plotted in Fig. 2, fits the experimental points very closely and accurately reproduces the position of the discontinuities.

The fact that different resonances and different devices can exhibit opposite types of nonlinearity shows that the nonlinear restoring force arises from the geometry of the vibrating structure and not the material.

**Conclusions:** The examination of the characteristics of different micromachined silicon structures for use as frequency-out sensors shows that large vibrational displacements may produce anharmonic behaviour, leading to asymmetrical resonance curves and eventually to hysteresis. These effects can restrict the use of noninterferometric interrogating techniques unless careful design of the resonator reduces the nonlinear response.

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**LONG-WAVELENGTH PINFET RECEIVER OEIC ON A GaAs-ON-InP HETEROSTRUCTURE**

*Indexing terms: Optoelectronics, Optical receivers*

A long-wavelength PINFET OEIC has been fabricated on a GaAs-on-InP heterostructure for the first time. A receiver sensitivity as high as $-31\, dBm$ for 600 Mbit/s NRZ has been obtained. The great potential of the GaAs-on-InP heterostructure for high-performance, long-wavelength OEIC applications has been demonstrated.

**Introduction:** The scope of optical fibre communications applications has expanded greatly from trunk line systems to subscriber systems. The long-wavelength InP-based optoelectronic integrated circuit (OEIC) with high performance and high reliability is one of the most important key elements for such new systems. InP-based OEICs, however, are still in the preliminary stage, in spite of their importance, due to the immaturity of InP-based electronic circuit technology.

The strained heteroepitaxial GaAs-InP structure is an attractive approach. This kind of structure has great potential for combining GaInAs(P)/InP long-wavelength optical components and the mature GaAs integrated circuit technologies. There have been several reports on fabricated InP-based materials grown on GaAs substrates (InP-on-GaAs structure). The InP-on-GaAs structure, however, involves a problem with regard to lattice mismatch between the InP-based optical components and the GaAs substrate. There is apprehension that the performance and reliability for minority-carrier devices, such as photodiodes and especially laser diodes, might deteriorate due to the lattice mismatch.

This letter proposes a new kind of heteroepitaxial structure: the GaAs-on-InP structure. This structure makes possible long-wavelength optical component integration on an InP substrate with perfect lattice matching. The GaAs electronic circuit is formed in the strained heteroepitaxial GaAs layers on the InP substrate. The device performance and reliability for majority-carrier devices, such as FETs, are hardly affected by the lattice mismatch. The first OEIC fabricated from the GaAs-on-InP structure is described.

**Fabrication:** Fig. 1 shows a cross-section of a receiver OEIC. The GaAs-on-InP structure wafer was prepared by a combination of VPE and MBE. First, GaInAs/InP PIN photodiode layers were grown on a stepped substrate by conventional VPE. The photodiode layers consisted of a 0.5-µm-thick (\( n \approx 5 \times 10^{13} \text{ cm}^{-2} \)) InP window layer, a 2-µm-thick (\( n \approx 5 \times 10^{15} \text{ cm}^{-2} \)) GaInAs absorbing layer and a 1-µm-thick (\( n \approx 5 \times 10^{15} \text{ cm}^{-2} \)) InP contact layer. After the wafer was preferentially mesa-etched, to form the PIN photodiode on the bottom part of the step-formed substrate, a 0.5-µm-thick undoped GaAs buffer layer and a 0.3-µm-thick Si-doped (\( n \approx 2 \times 10^{17} \text{ cm}^{-3} \)) GaAs channel layer were successively grown by strained heteroepitaxy by MBE at a 500°C substrate temperature.

Finally, the GaAs layers were preferentially mesa-etched to form a GaAs MESFET on the upper part of the stepped substrate.

The receiver OEIC was fabricated by a conventional OEIC process. After a planar PIN photodiode had been formed by selective Zn diffusion, the photodiode p-electrode was formed by Au-Zn and the photodiode n-electrode, MESFET source and MESFET drain were formed by Ni/Au-Ge, using the lift-off technique. The Al gate electrode was formed by a conventional recess gate process. Load resistances were also integrated monolithically by Ni-Cr evaporation.

**Device performance:** Fig. 2 shows a photograph of the chip and a circuit diagram for the fabricated receiver OEIC. A GaInAs PIN photodiode, GaAs MESFET and two load resistances were monolithically integrated. The chip size is 500 x 700 µm. The PIN photodiode photosensitive area is 60 µm in diameter. The FET gate length and gate width are 1.8 µm and 280 µm, respectively. The PIN photodiode load resistance and FET load resistance are 3 kΩ and 570Ω, respectively.

The dark current characteristics for the integrated GaInAs PIN photodiodes were measured. At <5V reverse bias the